ARM® Cortex®-M0+ core-based processor family for sensorless BLDC and PMSM motor control

Kinetis KV1x MCU Family

The Kinetis KV1x family of microcontrollers is a high-performance, cost-competitive solution for three-phase sensorless BLDC and PMSM motor control applications and the entry point into the Kinetis V series—the first Kinetis microcontroller family specifically designed for motor control.

TARGET APPLICATIONS

▸ Sensorless BLDC motor control

▸ Entry-level sensorless PMSM motor control

▸ Compressors

▸ Pumps

▸ Domestic appliances

Built upon the Cortex-M0+ core running at 75 MHz with hardware square root and divide capability, Kinetis KV1x microcontrollers deliver a 27% increase in performance in math-intensive applications versus comparable MCUs, allowing them to target BLDC as well as more computationally demanding PMSM motors. Additional features include integrated FlexCAN, dual 16-bit analog-to-digital controllers (ADCs) sampling at up to 1.2 mega samples per second (MS/s) in 12-bit mode, multiple motor control timers, up to 128 KB of flash memory and a comprehensive enablement suite both from us and third-party resources, including reference designs, software libraries and motor configuration tools.

KINETIS KV1x MCU FAMILY

Part Number	CPU	Pin Count	Package	Flash	SRAM	FlexCAN	FlexTimers
MKV11Z128	75	64, 48, 32*	LQFP	128	16	1	2 x 6-ch., 4 x 2-ch.
		32	QFN				
MKV10Z128		64, 48, 32*	LQFP			0	
		32	QFN				
MKV11Z64		64, 48, 32*	LQFP	64		1	
		32	QFN				
MKV10Z64		64, 48, 32*	LQFP			0	
		32	QFN				
MKV10Z32		48, 32	LQFP	32	8	0	1 x 6-ch., 2 x 2-ch.
		32	QFN				
MKV10Z16		48, 32	LQFP	16			
		32	QFN				

*This package is included in the Package Your Way program for Kinetis MCUs. For more details, please visit **www.nxp.com/KPYW.**

FEATURES AND BENEFITS

▸ 75 MHz Cortex-M0+ core with hardware square root and divide block that improves performance in math-intensive applications (e.g., processing of sensorless field-oriented control (FOC) algorithms)

▸ 2 x 16-bit ADCs with two capture and hold circuits and up to 1.2 MS/s samples rate in 12-bit mode, simultaneous measurement of current and voltage phase, reduced jitter on input values improving system accuracy

▸ Up to 2 x 6-channel and 4 x 2-channel programmable FlexTimers—High-accuracy PWM generation with integrated power factor correction or speed sensor decoder (incremental decoder/hall sensor)

▸ 12-bit DAC and 2 x ACMP (analog comparators) for overcurrent and overvoltage fault detection and reduced BOM costs; ADC and ACMP interconnect with PWM and PDB (programmable delay) blocks for real-time hardware control

▸ 4-channel DMA—reduced CPU loading for improved application performance

▸ Dual watchdogs—compliance with IEC 60730 safety regulation requirements

▸ Broad family scalability with hardware and software compatibility—easy migration to more performance, memory and feature integration within the Kinetis V series
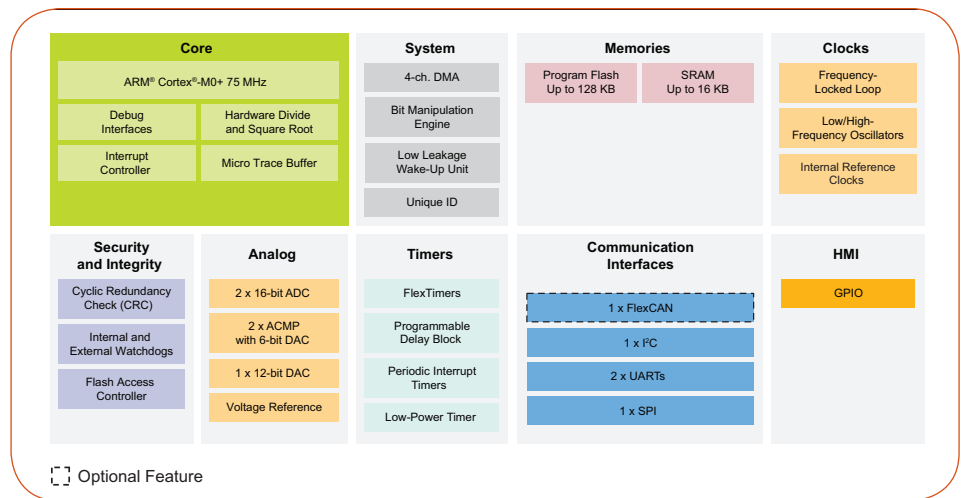
DEVELOPMENT TOOLS

TWR-KV10Z32 and TWR-KV11Z75M

The Tower System MCU module is a cost-effective, modular development platform that features the KV1x MCU in either a 48 LQFP or 64 LQFP package, integrated OpenSDA debug adapter (requires no external debug interface) and is compatible with the TWR-MC-LV3PH three-phase motor peripheral module.

HVP-MC3PH

The HVP-MC3PH platform enables development of three-phase PMSM, BLDC and ACIM motor control and power factor

KINETIS KV1x MCU FAMILY BLOCK DIAGRAM

Core
- ARM® Cortex®-M0+ 75 MHz
- Debug Interfaces
- Hardware Divide and Square Root
- Interrupt Controller
- Micro Trace Buffer

System
- 4-ch. DMA
- Bit Manipulation Engine
- Low Leakage Wake-Up Unit
- Unique ID

Memories
- Program Flash Up to 128 KB
- SRAM Up to 16 KB

Clocks
- Frequency-Locked Loop
- Low/High-Frequency Oscillators
- Internal Reference Clocks

Security and Integrity
- Cyclic Redundancy Check (CRC)
- Internal and External Watchdogs
- Flash Access Controller

Analog
- 2 x 16-bit ADC
- 2 x ACMP with 6-bit DAC
- 1 x 12-bit DAC
- Voltage Reference

Timers
- FlexTimers
- Programmable Delay Block
- Periodic Interrupt Timers
- Low-Power Timer

Communication Interfaces
- 1 x FlexCAN
- 1 x I²C
- 2 x UARTs
- 1 x SPI

HMI
- GPIO

☐ Optional Feature

correction (PFC) solutions in a safe high-voltage environment.

Compatible with the Kinetis KV10 MCU and KV11 MCU (and several other proprietary controllers), input voltage is 85–240 V AC, with output power of the motor stage up to 1 KW, with the ability to drive a 1.2 Hp motor, and 800 watts when utilizing the PFC stage.

FRDM-KV10Z

The FRDM-KV10Z is an ultra-low-cost development platform for Kinetis KV1x MCUs. The FRDM-KV10Z hardware is form-factor compatible with the Arduino™ R3 pin layout, providing a broad range of expansion board options, including FRDM-MC-LVPMSM and FRDM-MC-LVBLDC for permanent magnet and brushless DC motor control.

Integrated Development Environments (IDEs)

Kinetis KV1x MCUs will be supported by the Kinetis Design Studio IDE, IAR Embedded Workbench® for ARM and ARM Keil® Microcontroller Development Kit. All IDEs support the Processor Expert auto code generator—a GUI-based, device-aware software configuration tool that

automatically generates peripheral startup code and device drivers to dramatically reduce application development time.

FreeMASTER

FreeMASTER is a free, simple, yet highly customizable real-time debug monitor and data visualization tool designed for software development that requires real-time data access.

Motor Control Toolbox

Our motor control development toolbox is a comprehensive collection of tools that plug into the MATLAB™/Simulink™ model-based design environment to support rapid application development targeting our MCUs.

Embedded Motor Control and Power Conversion Libraries

▸ Extensive suite of complimentary software libraries for motor and power control applications

▸ A group of algorithms, ranging from basic mathematics operations to advanced transformations and observers, which can easily be incorporated into complex real-time control applications

▸ Core self-test libraries for simpler IEC 60730 certification

www.nxp.com/Kinetis/Vseries